This Page Is Inserted by IFW Operations and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

0

IMAGES ARE BEST AVAILABLE COPY.

As rescanning documents will not correct images, please do not report the images to the Image Problem Mailbox.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of

Akio KITAMURA

Serial No.: 10/055,722

Group Art Unit: 2811

Filed: January 23, 2002

Examiner:

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND

MANUFACTURE METHOD THEREFORE

Certificate of Mailing

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450 on:

Date: 03/31/09

Ву: ____

Marc A. Rossi`

REQUEST FOR ACKNOWLEDGMENT OF INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

Applicant hereby requests that the Patent Office acknowledge the receipt of the Information Disclosure Statement filed on January 23, 2002 for the above-referenced application. For the convenience of the Patent Office, a copy of the Information Disclosure Statement, the PTO Form-1449 and the stamped Receipt Card are enclosed. It is respectfully requested that an initialed copy of the PTO-1449 be returned to the undersigned.

Respectfully submitted,

ROSSI & ASSOCIATES

03/31/69

Date

Marc A Rossi

Registration No. 31,923

Attorney Docket No. FUJI:203

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Akio KITAMURA

Filed: January 23, 2002

Serial No.: NEW APPLICATIO

Batch:

Group Art Unit:

Examiner:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND For:

MANUFACTURE METHOD THEREFORE

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. Copies of the references listed on Form PTO-1449 are attached.

It is respectfully requested that the information be expressly considered during the prosecution of this application, that these references be made of record therein and appear among the "References Cited" on any patent to issue therefrom, and that an initialed copy of the PTO-1449 be returned to the undersigned.

Respectfully submitted,

Date: _____01/23/07

Registration No. 31,923

Attorney Docket No.: FUJI:203

ROSSI & ASSOCIATES

P.O. Box 826

Ashburn, VA 20146-0826

					Docket Number (Optional) Application Number				
INFORMATION DISCLOSURE CITATION					FUJI:20. Applicant(s)		NEW APPLICATION		
(Use several sheets if necessary) Akio KITAMURA									
APR 0 1 2004 (5)					Filing Date January 23, 2002		Group Art Unit		
					<u>'</u>				
U.S. PATENT DOCUMENTS *EXAMINER RETRADEMED DOCUMENT NUMBER DATE NAME CLASS SUBCLASS FILING DATE									
*EXAMINER INITIAL	Ref	HADE MOCUMENT NUMBER	DATE	NAME		CLASS	SUBCLASS	ı	DATE OPRIATE
						(COPY		
	\ <u></u>								
									
					<u></u>				
						-			
				_					
FOREIGN PATENT DOCUMENTS									
	REF	EF DOCUMENT NUMBER DATE			COUNTRY		SUBCLASS	Trans YES	slation NO
	JP	60-10780	1/85 JAPAN		(English Abst.)			1	
	JP	60-105277	6/85	JAPAN (English Abst.)				1	
	JP	61-190983	8/86	JAPAN (English Abst.)				1	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)									
									······································
EXAMINER					DATE CONSIDERED				
EXAMINER	R: Initia	al if citation considered, whether	or not citation is i	n conformanc	e with MPEP Section 609;	Draw line throug	h citation if not	in conform	ance and
not considered. Include copy of this form with next communication to applicant.									

Form PTO-A820 (also form PTO-1449)



USPT RECEIPT CARD In re application of:

Akio KITAMURA

Batch:

Serial No.: NEW APPLICATION Group Art Unit:

Filed: January 23, 2002

Examiner:

SEMICONDUCTOR INTEGRATED CIRCUIT For:

DEVICE AND MANUFACTURE METHOD

THEREFORE

Papers Filed: NEW APPLICATION; SPECS. 20 PGS.; FIGS. 1-14; IDS; PRIORITY

Attorney Docket: FUJI:203